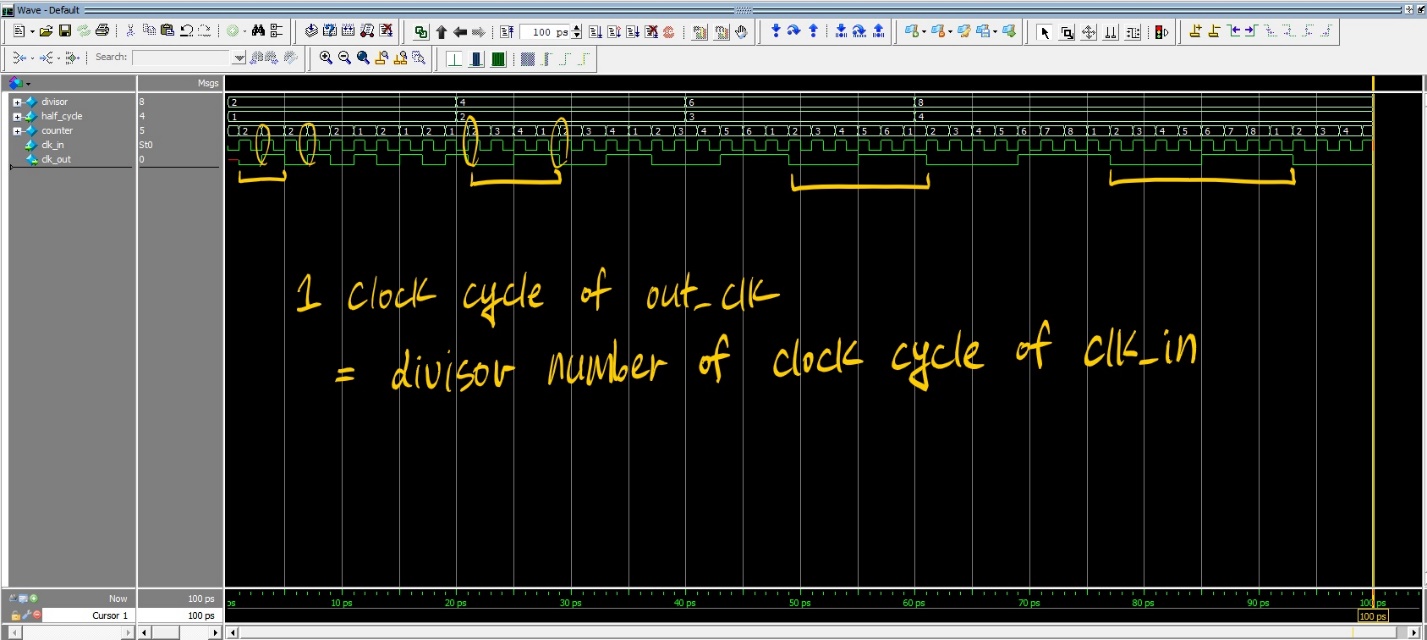
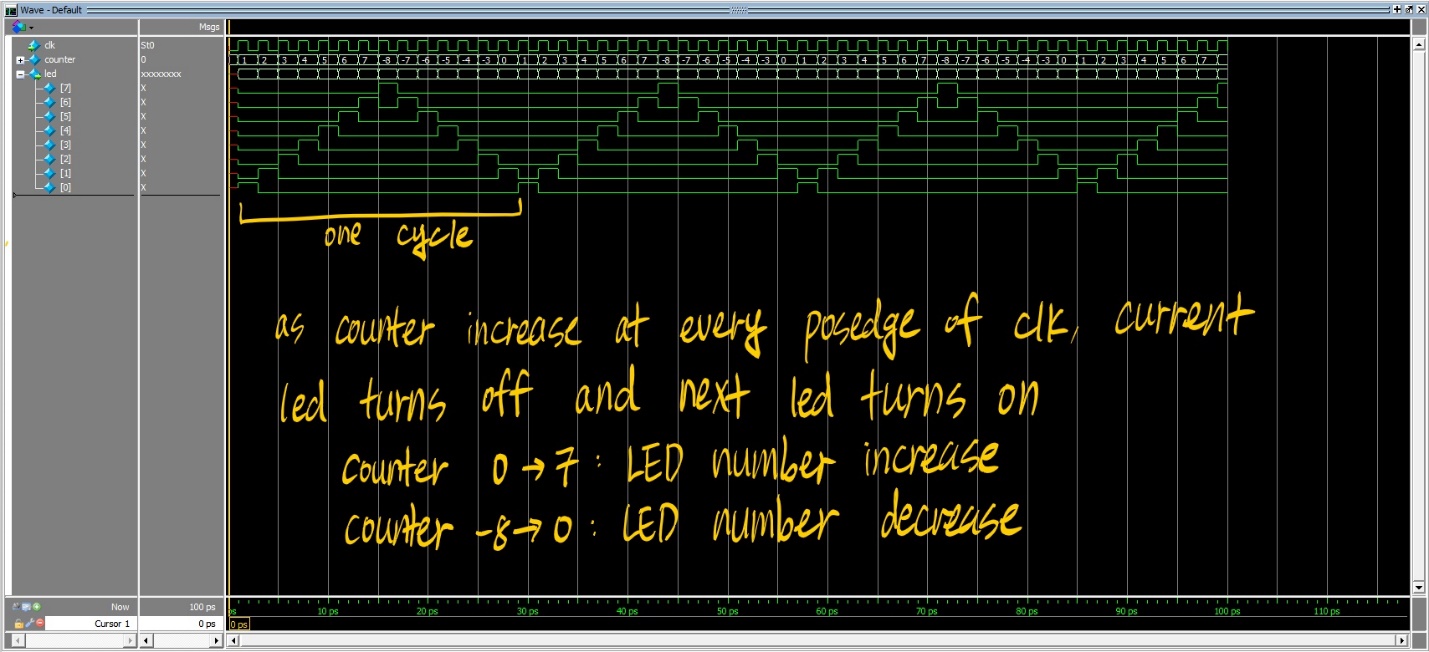
1. Where (which directory path) is your SOF file located, and what it is called
   1. Location of SOF **~/Cassiel\_Jung\_33294968\_Lab\_Lab1.zip/rtl/lab1\_template\_de1soc**
   2. Name of SOF

**Basic\_Organ\_Solution.sof**

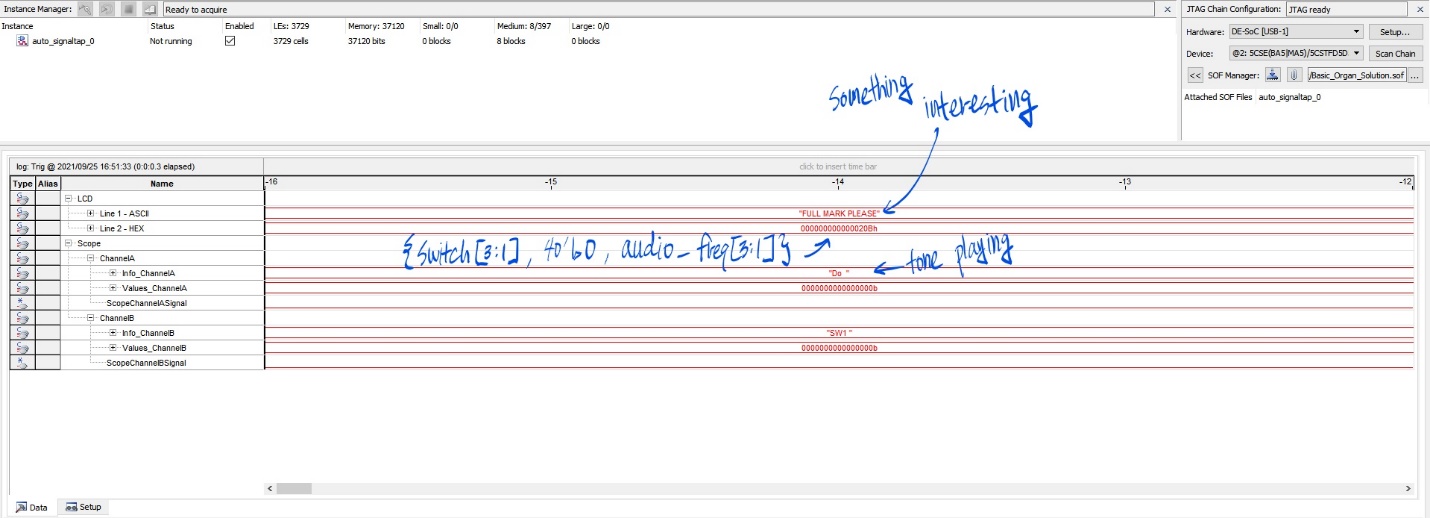
1. What is the status of the lab (what works, what doesn't)
   1. Everything works. Frequency divider, the tone organ and LED light up from side to side
2. Annotated simulation screenshots as required by the lab
   1. clockDivisor



* 1. movingLED



1. Annotated SignalTap screenshots as required by the lab



1. Information on how to run the simulations (i.e. where the files are located, which program you used to run your simulation)
   1. Simulation files are located at **~/Cassiel\_Jung\_33294968\_Lab\_Lab1.zip/sim**
   2. ModelSim was used.
   3. Steps
      1. Open project file named **lab1\_simulation.mpf**
      2. Compile all
      3. Start simulation with **clockDivisor\_tb.v** or **movingLED\_tb.v**
      4. Load .do file of **clcokDivisor\_wave\_format.do** or **movingLED\_wave\_format.do**
      5. Run the simulation
2. Any additional information that would be relevant for the TA marking your project.
   1. Annotated images are included in **~/Cassiel\_Jung\_33294968\_Lab\_Lab1.zip/sim** as well